

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3849	(ic (integrat\$3 near3 circuit\$1) processor\$1 cpu microprocessor\$1 controller\$1) with (each different\$3 separat\$3 another\$1 other\$1 many) with ((secret\$2 near3 information\$1) (secret\$2 near3 data\$1) key\$1 password\$1 encrypt\$4 cipher\$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L2	343410	(location\$1 area\$1 address\$3 module\$1 pin\$1 slot\$1) near6 (memor\$3 storage\$1 rom ram eeprom eeprom)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L3	69620	(ic (integrat\$3 near3 circuit\$1)) with (processor\$1 cpu microprocessor\$1 controller\$1)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L4	12507	L2 with ((secret\$2 near3 information\$1) (secret\$2 near3 data\$1) key\$1 password\$1 encrypt\$4 cipher\$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L5	1540	L4 and L3	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L6	381	L5 and (process\$2 near4 (same\$1 identical\$2 mirror\$1))	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L7	67	L6 and L1	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50

L8	40133	(ic chip\$1 (integrat\$3 near3 circuit\$1)) near6 ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L9	62815	L3 and process\$2	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L10	14062	((713/152) or (713/189) or (713/190) or (713/192) or (713/193) or (713/375) or (713/400) or (726/34) or (711/1) or (711/5) or (711/100) or (711/101-105) or (711/111) or (711/147) or (711/148) or (711/150) or (711/152) or (71/209) or (380/201) or (380/228) or (380/240) or (380/255)).CCLS.	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L11	1938	L10 and L9	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L12	510	L11 and L8	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L13	22442	(ic chip\$1 (integrat\$3 near3 circuit\$1)) near8 (location\$1 area\$1 address\$3 module\$1 pin\$1 slot \$1) near4 (memor\$3 storage\$1 rom ram eeprom eeprom)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50

L14	33	L13 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9) with (each different\$3 separat\$3 another\$1 other\$1 many)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L15	8	L12 and L14	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L16	150	L13 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1) with (each different\$3 separat\$3 another\$1 other\$1 many)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L17	46	L9 and L16	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L18	46	(ic (integrat\$3 near3 circuit\$1)) and L17	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L19	24	L18 and ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L20	1473	L4 and L9	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L21	51888	L2 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L22	5712	L9 and L13	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L23	2125	L8 and L22	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L24	203	L12 and L13	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50

L25	25	L24 and ((tamper\$3 obscur\$4 fault\$1 unsafe malicious\$2 insecure \$4 secur\$6 unsecur \$6) near5 (detect\$4 monitor\$3 scan\$4))	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L26	3708	(tamper\$3 obscur\$4 fault\$1 fraud\$7 unsafe malicious\$2 insecur\$4 secur\$6 unsecur\$6) near5 (detect\$4 monitor\$3 scan\$4) near8 (memor\$4 rom ram eprom eeprom storage\$1)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L27	12	L24 and L26	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L28	386	(ic chip\$1 (integrat\$3 near3 circuit\$1)) with (tamper detect\$4 obscur\$9 intrusi\$4 intrud\$4) near4 (memor\$3 storage\$1 rom ram eeprom eprom nonvolatile\$1 non-volatile\$1) with (process\$3 microprocess\$3 micro-processor\$1 cpu)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L29	187378	(process\$2 execut\$4 run\$4 code\$1 instruc \$4) near7 (same\$1 identical\$2 mirror\$1) near6 (software\$1 program\$3 process \$2)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L30	142907	(process\$2 execut\$4 run\$4 code\$1 instruc \$4) near4 (same\$1 identical\$2 mirror\$1) near4 (software\$1 program\$3 process \$2)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L31	112	L28 and L30	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50

L32	13256	L2 with ((secret\$2 near3 information\$1) (secret\$2 near2 data \$1) (secur\$7 near2 data\$1) (secur\$7 near2 information\$1) (secret\$2 near2 content\$1) (secur\$7 near2 content\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L33	468	L13 with ((secret\$2 near3 information\$1) (secret\$2 near2 data \$1) (secur\$7 near2 data\$1) (secur\$7 near2 information\$1) (secret\$2 near2 content\$1) (secur\$7 near2 content\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L34	2	L31 and L33	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L35	113	(ic chip\$1 (integrat\$3 near3 circuit\$1)) with (tamper detect\$4 obscur\$9 intrusi\$4 intrud\$4) near4 (memor\$3 storage\$1 rom ram eeprom eeprom nonvolatile\$1 non-volatile\$1) with ((secret\$2 near3 information\$1) (secret \$2 near2 data\$1) (secur\$7 near2 data \$1) (secur\$7 near2 information\$1) (secret \$2 near2 content\$1) (secur\$7 near2 content\$1) key\$1 password\$1 encrypt \$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L36	66	L31 and L35	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50

L37	4202	(tamper\$3 obscur\$4 fault\$1 fraud\$7 unsafe malicious\$2 insecur\$4 secur\$6 unsecur\$6) near8 (detect\$4 monitor\$3 scan\$4) near8 (memor\$4 rom ram eeprom eeprom storage\$1)	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L38	66	L36 and L37	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L39	66	L38 and tamper\$4	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L40	329	simon near3 walmsley	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L41	96	L40 and L3	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L42	11	L41 and L37	USPAT; IBM_TDB	OR	OFF	2009/04/02 15:50
L43	2	(integrate\$1 and circuit\$1 and tamper and line and memory and processor\$1 and chip\$1).dm.	US-PGPUB	OR	ON	2009/04/02 15:50
L44	1	(integrate\$1 and circuit\$1 and location \$1 and non-volatile and plurality and secret\$1 and memory and processor\$1 and chip\$1).dm.	US-PGPUB	OR	ON	2009/04/02 15:50
L45	174	tamper\$3 near5 detect\$4 near3 line\$1	US-PGPUB	OR	ON	2009/04/02 15:50
L46	156	L45 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2009/04/02 15:50
L47	144	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	US-PGPUB	OR	ON	2009/04/02 15:50
L48	423	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	US-PGPUB	OR	ON	2009/04/02 15:50

L49	268	L48 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2009/04/02 15:50
L50	124	L49 not L47	US-PGPUB	OR	ON	2009/04/02 15:50
L51	195	tamper\$3 near5 detect\$4 near3 line\$1	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L52	124	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L53	329	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L54	325	L53 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L55	124	L52 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L56	325	L54 not L47	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L57	148	L56 and (obscure\$4 obfuscate\$4)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L58	362	(obscure\$4 obfuscate\$4) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L59	2	L56 and L58	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L60	0	((tamper\$3 intruder\$4 intruder\$3) near5 detect \$4 near3 (circuit\$3line \$1)) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/04/02 15:50

L61	182	((tamper\$3 intrud\$4 intrud\$3) near5 detect \$4 near3 (circuit\$3 line\$1)) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L62	181	L61 not L47	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:50
L63	4863	(obscure\$4 obfuscate \$4) near5 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:51
L64	5647	(obscure\$4 obfuscate \$4) near8 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:51
L65	1	L62 and L64	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:51
L66	59944	(decrypt\$9 decipher \$9 decode\$3 obfuscate \$3 obscure\$9) near6 (operation\$1 attempt \$4 access\$3)	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:51
L67	119	L62 and L66	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:51
L68	107	L67 and (ic (integrate \$1 near3 circuit\$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 15:51
L69	99	L68 and (flash near3 (storage\$1 memory \$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 16:08
L70	2975	(flash near3 (storage \$1 memory\$1)) near8 (ic (integrate\$1 near3 circuit\$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 16:45
L72	4	L69 and 70	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 16:45
L73	130	(flash near3 (storage \$1 memory\$1)) near4 ((different\$2 various) near4 (location\$1 area\$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 16:48
L74	130	73 not 72	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 16:48
L75	18	73 and 70	USPAT; EPO; IBM_TDB	OR	ON	2009/04/02 17:05



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